

IN THE CLAIMS:

Please amend Claims 1 to 9 as shown below. The claims, as pending in the subject application, read as follows:

1. (Currently Amended) A method of controlling a semiconductor device signal generator using a microcomputer having a timer, ~~the signal generator outputting a control signal for controlling a timing of turning on/off a semiconductor device by means of the timer,~~ the method comprising:

an interrupting step of interrupting the semiconductor device using the timer, by generating a control signal for controlling a timing of turning on/off the semiconductor device,

wherein the interrupting step is performed in accordance with ~~started by an~~ interrupting signal which is input to the microcomputer at every occurrence of a predetermined period,

and wherein the interrupting step comprises a setting step of giving a set value to the timer and a calculating step of determining a set value for a subsequent interrupting step, such that the set value determined in the calculating step is given to the timer in the setting step of the subsequent interrupting step ~~wherein the interrupting step comprises at least the setting step of setting a set value for the timer, the set value being calculated in a pre-interrupting step preceding the interrupting step, and the calculating step of determining a set value for the timer, the set value being used in a post-interrupting step succeeding the interrupting step, and the setting step is performed before the calculating step in each of the interrupting steps.~~

2. (Currently Amended) The method of controlling the semiconductor device ~~signal generator~~ according to claim 1, wherein the ~~interrupting step~~ comprises the setting step of setting of the interrupting step sets a first timing of turning on/off the semiconductor device ~~for the timer~~, the first timing being calculated in the calculating step of a ~~preceding interrupting step~~ pre-interrupting step preceding the interrupting step by the predetermined period,

and wherein the calculating step of calculating of the interrupting step calculates a second timing of turning on/off the semiconductor device, the second timing being set for the timer in the setting step of the subsequent interrupting step ~~a post-interrupting step~~ succeeding the interrupting step by the predetermined period.

3. (Currently Amended) A method of controlling [[a]] at least first and second semiconductor devices ~~signal generator~~ using a microcomputer having a timer, the ~~signal generator outputting a control signal for controlling a timing of turning on/off at least first and second semiconductor devices by means of the timer~~; the method comprising:

an interrupting step of interrupting the at least first and second semiconductor devices using the timer, by generating a control signal for controlling a timing of turning on/off the at least first and second semiconductor devices.

wherein the interrupting step is performed in accordance with started by an interrupting signal which is input to the microcomputer at every occurrence of a predetermined period,

and wherein the interrupting step comprises a setting step of giving a set

value to the timer and a calculating step of determining a set value for a subsequent interrupting step, such that the set value determined in the calculating step is given to the timer in the setting step of the subsequent interrupting step wherein the interrupting step comprises the calculating step of calculating a timing of turning on/off the first and the second semiconductor devices, and the setting step of setting, for the timer, the calculated timing of turning on/off the first and the second semiconductor devices after an on/off control signal of the first semiconductor device is outputted.

4. (Currently Amended) The method of controlling the semiconductor device signal generator according to any one of claims 1 to 3, wherein the signal generator controls the semiconductor device of a power converter is controlled.

5. (Currently Amended) The method of controlling the semiconductor device signal generator according to claim 4, wherein the power converter controlled by the signal generator is a power conditioner for photovoltaic power generation.

6. (Currently Amended) A computer-executable program stored on a computer readable medium, the program for performing the method of controlling the semiconductor device signal generator according to any one of claims 1 to 3.

7. (Currently Amended) A computer-executable program stored on a computer readable medium, the program for performing the method of controlling the semiconductor device signal generator according to claim 4.

8. (Currently Amended) A recording computer readable medium which stores the a computer-executable program for performing the method of controlling the semiconductor device according to any one of claims 1 to 3 claim 6.

9. (Currently Amended) A recording computer readable medium which stores the a computer-executable program for performing the method of controlling the semiconductor device according to of claim 4 [[7]].